

Attorney Docket No. WIDC-021/00US
Express Mail No.

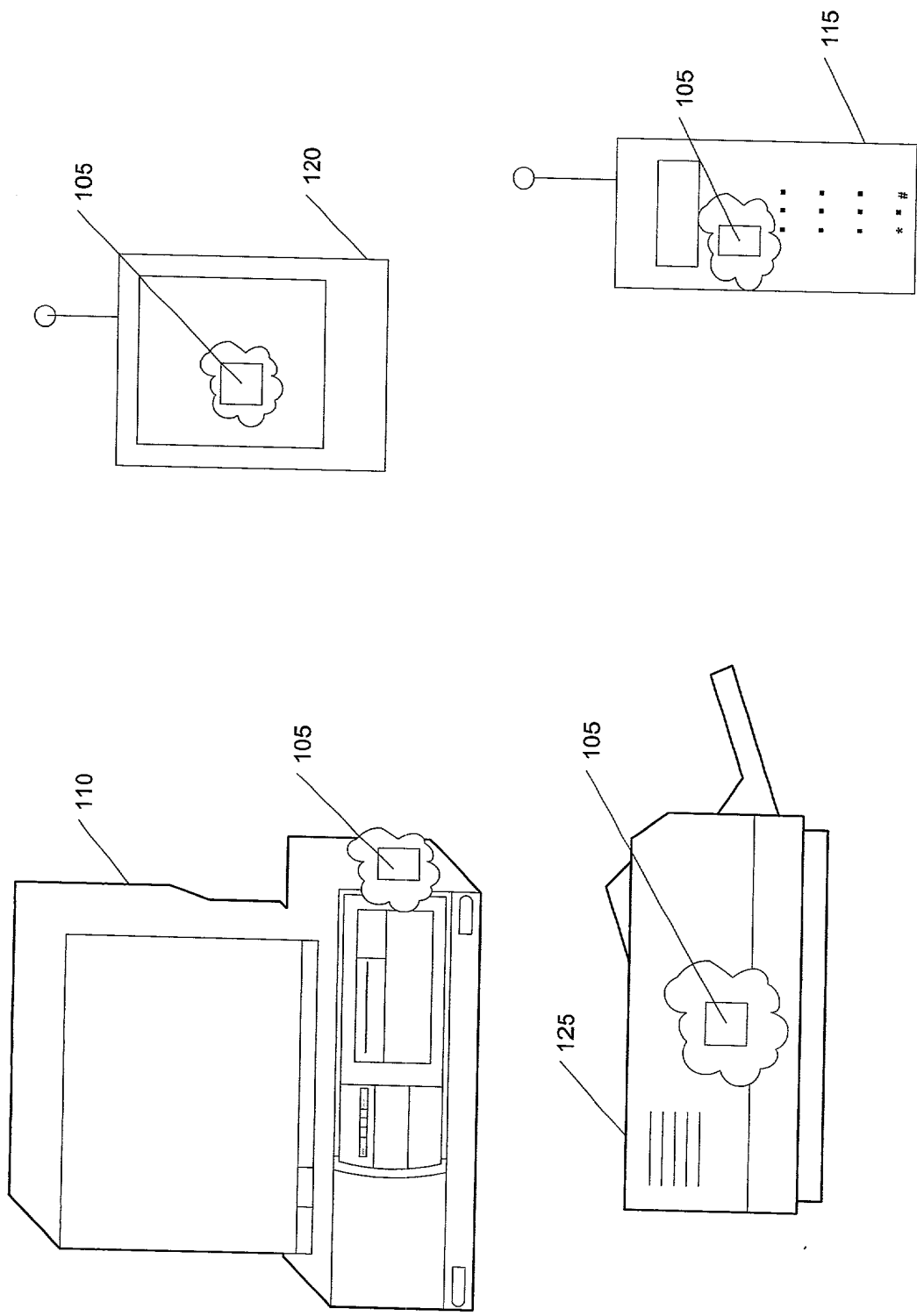
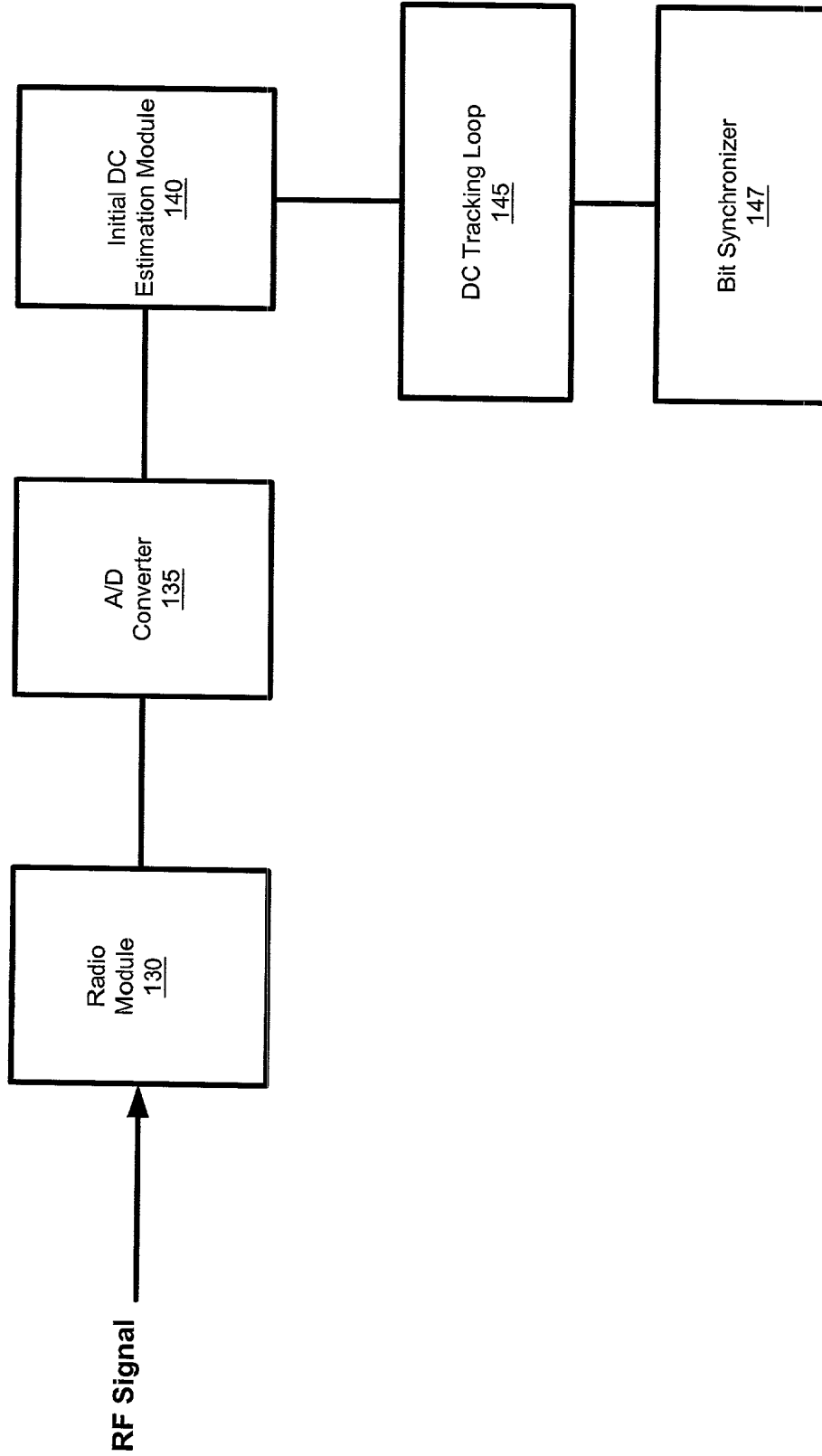


FIGURE 1.

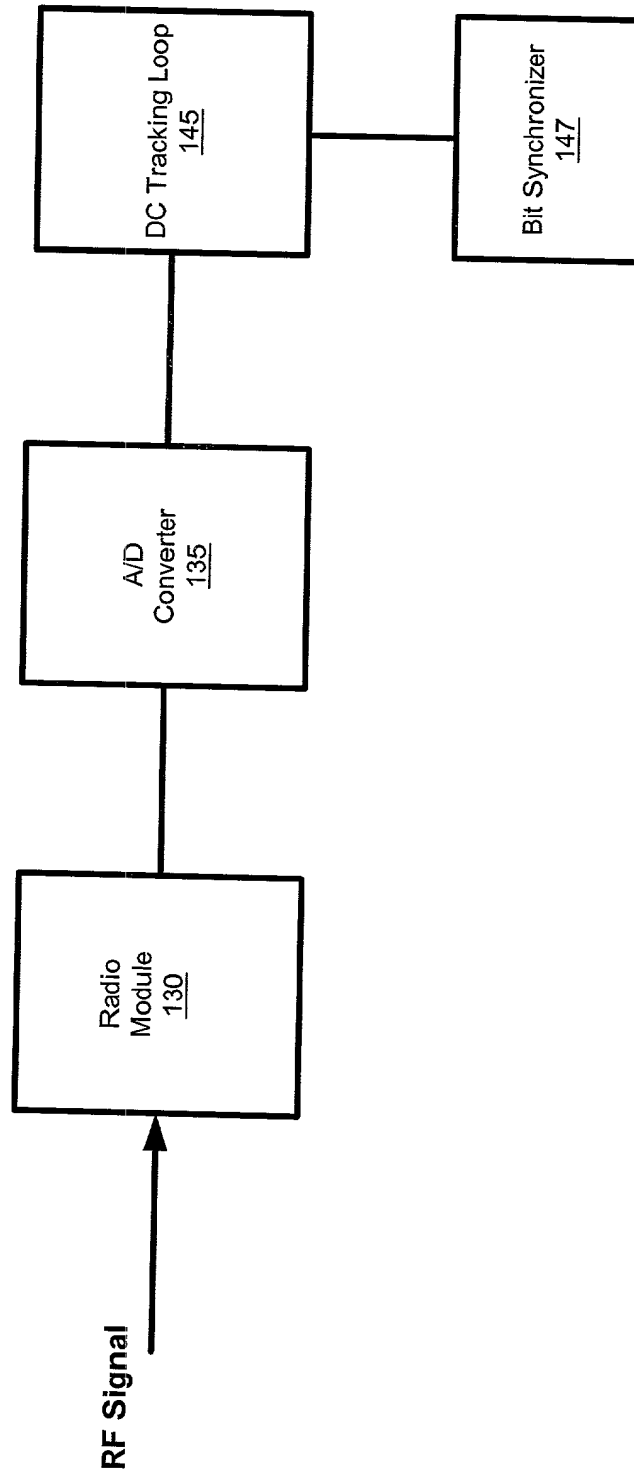
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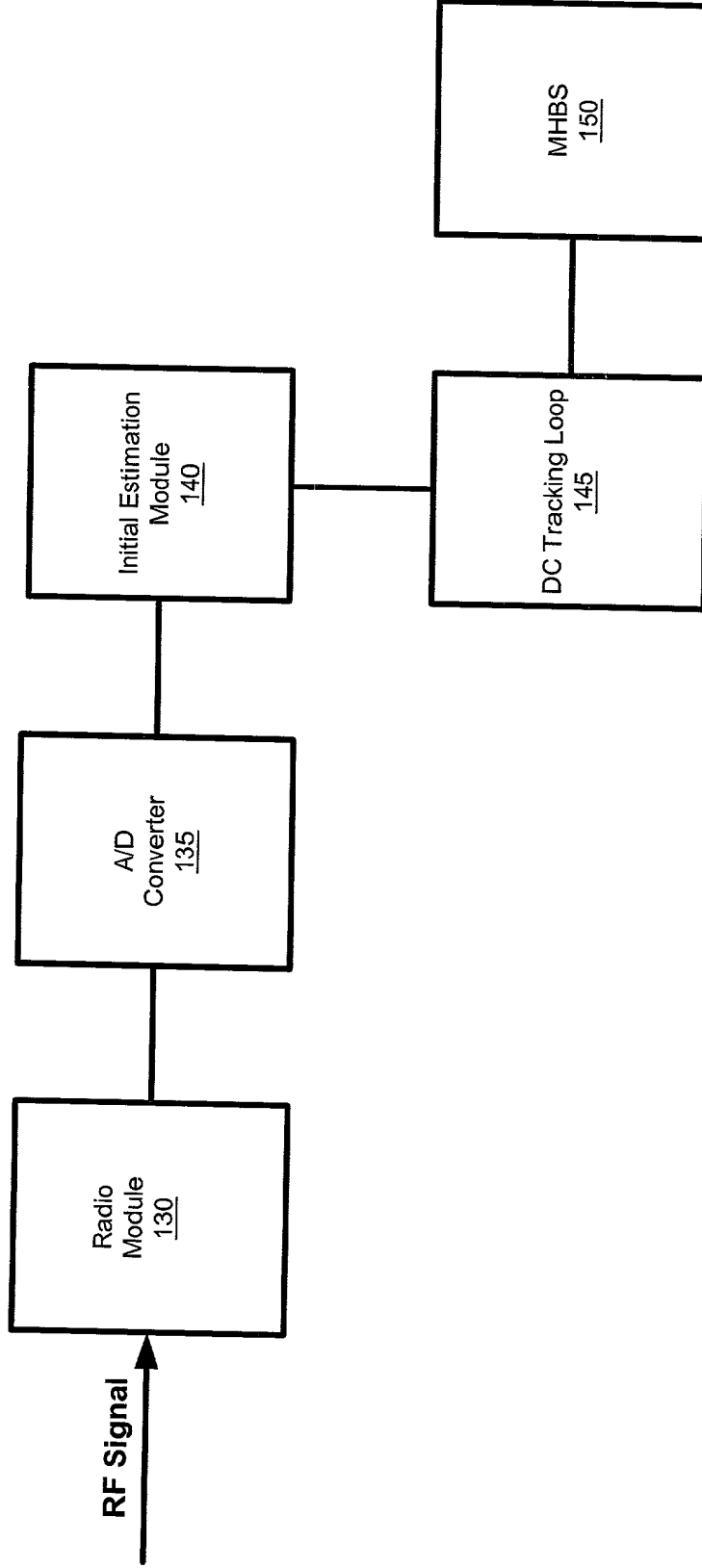


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the date of the court order.

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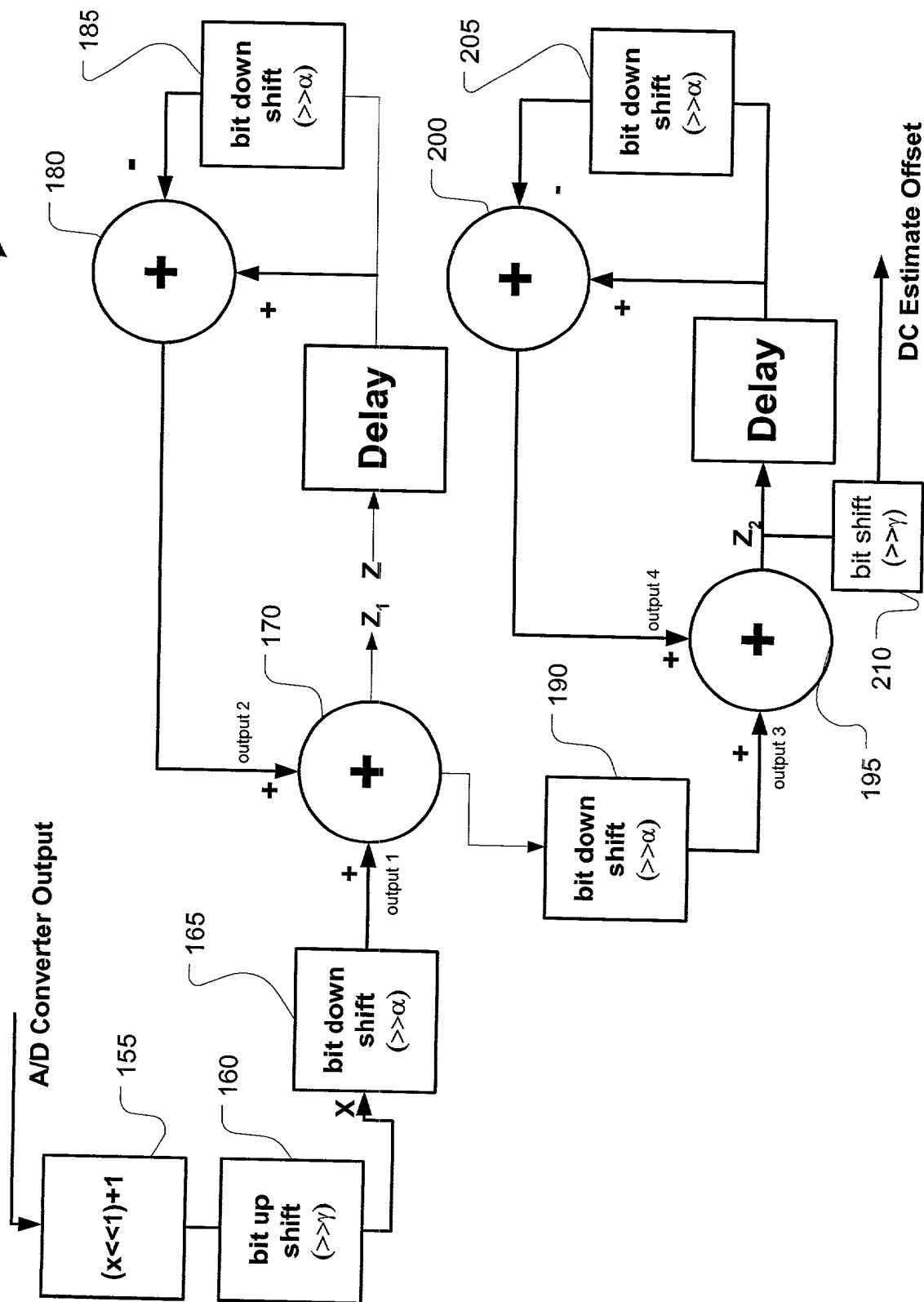
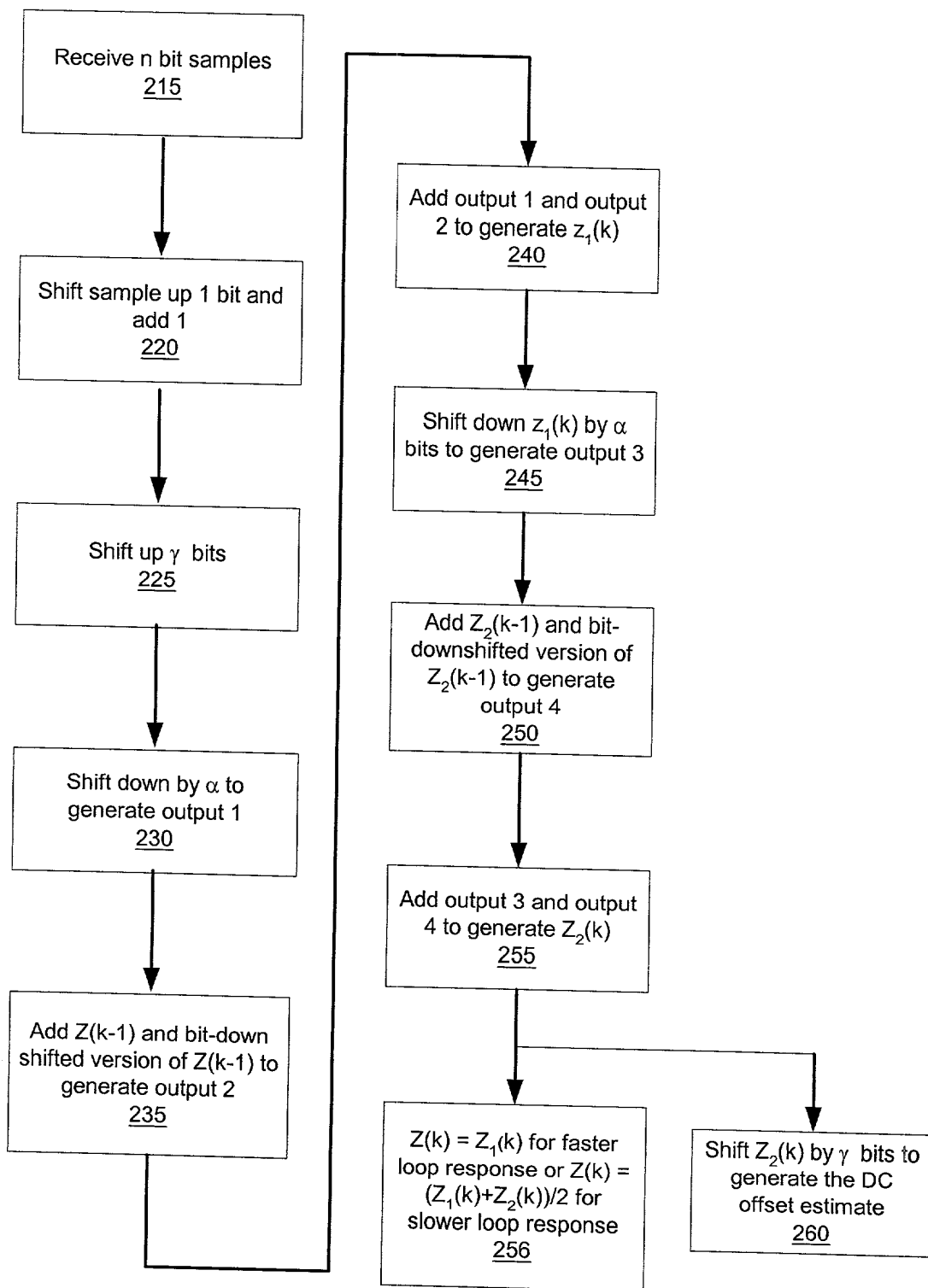


FIGURE 5.



When a signal is input to the system, the system will output a signal that is the sum of the input signal and the output of the DC offset compensator.

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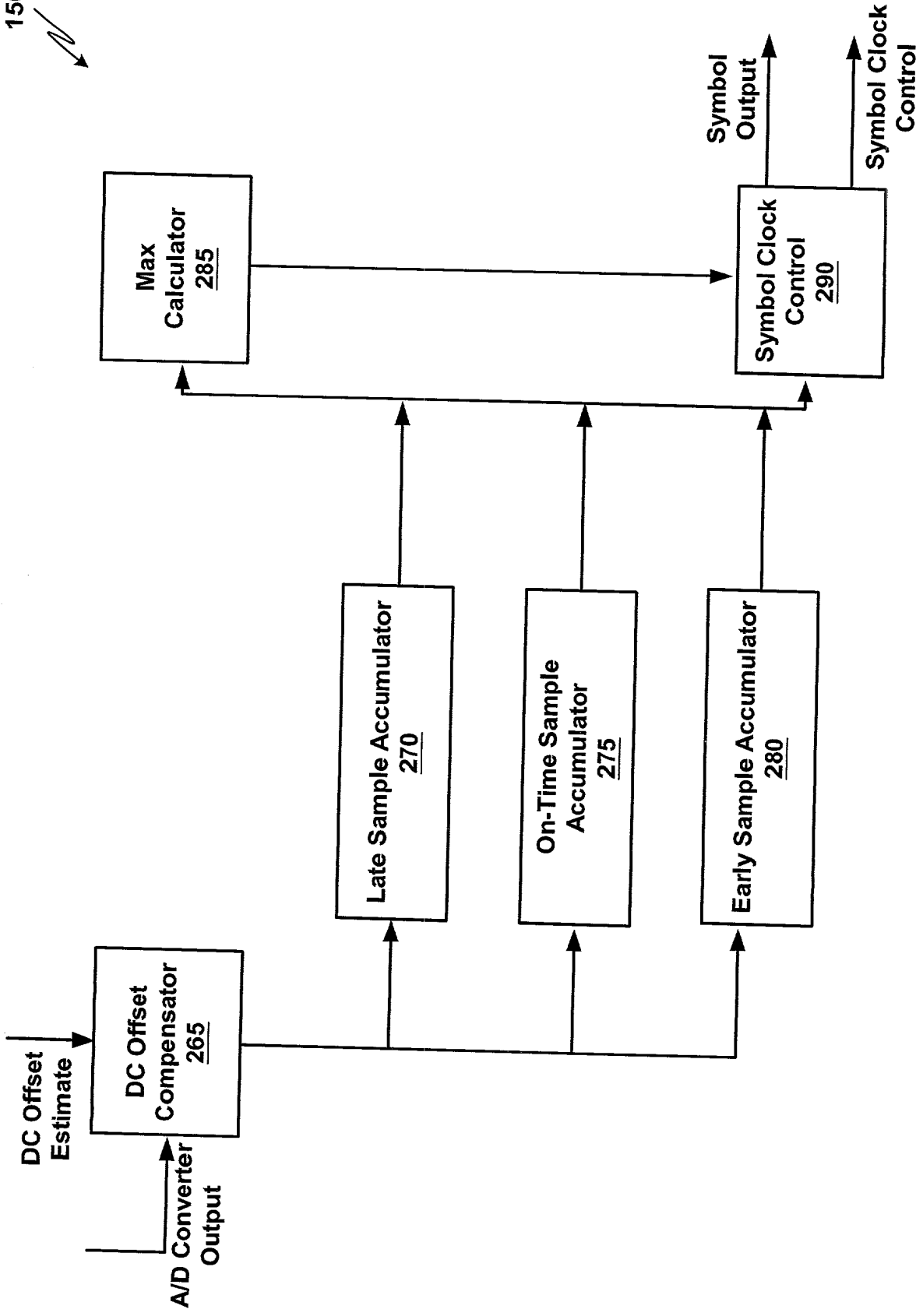
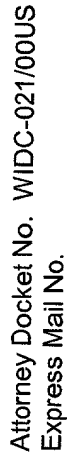


FIGURE 7.



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